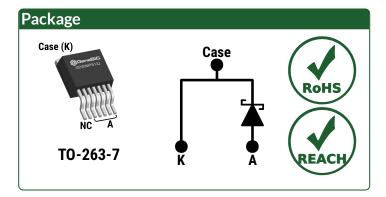
GeneSic SEMICONDUCTOR

Silicon Carbide Schottky Diode

 V_{RRM} = 1200 V $I_{F(T_C = 150^{\circ}C)}$ = 30 A Q_C = 97 nC

Features

- Gen4 Thin Chip Technology for Low V_F
- Superior Figure of Merit Qc*V_F
- 100% Avalanche (UIL) Tested
- Enhanced Surge Current Withstand Capability
- Temperature Independent Fast Switching
- Low Thermal Resistance
- Positive Temperature Coefficient of V_F
- High dV/dt Ruggedness



Advantages

- Improved System Efficiency
- High System Reliability
- Optimal Price Performance
- Reduced Cooling Requirements
- Increased System Power Density
- Zero Reverse Recovery Current
- Easy to Parallel without Thermal Runaway
- Enables Extremely Fast Switching

Applications

- Power Factor Correction (PFC)
- Electric Vehicles and Battery Chargers
- Solar Inverters
- High Frequency Converters
- Switched Mode Power Supply (SMPS)
- Motor Drives
- Anti-Parallel / Free-Wheeling Diode
- Induction Heating & Welding

Absolute Maximum Ratings (At T _C = 25°C Unless Otherwise Stated)										
Parameter	Symbol	Conditions	Values	Unit	Note					
Repetitive Peak Reverse Voltage	V_{RRM}		1200	٧						
Continuous Forward Current	l _F	$T_C = 100^{\circ}C$, D = 1	59							
		$T_C = 135^{\circ}C$, D = 1	41	Α	Fig. 4					
		$T_C = 150^{\circ}C$, D = 1	30							
Non-Repetitive Peak Forward Surge Current, Half Sine Wave	Іҕѕм	T_C = 25°C, t_P = 10 ms	240	۸						
		T_C = 150°C, t_P = 10 ms	192	Α						
Repetitive Peak Forward Surge Current, Half Sine Wave	I _{F,RM}	T_C = 25°C, t_P = 10 ms	144	Α						
		T_C = 150°C, t_P = 10 ms	100	А						
Non-Repetitive Peak Forward Surge Current	I _{F,MAX}	T_C = 25°C, t_P = 10 μ s	1200	Α						
i ² t Value	∫i²dt	T_C = 25°C, t_P = 10 ms	288	A ² s						
Non-Repetitive Avalanche Energy	E _{AS}	$L = 0.6 \text{ mH}, I_{AS} = 30 \text{ A}$	271	mJ						
Diode Ruggedness	dV/dt	$V_R = 0 \sim 960 \text{ V}$	200	V/ns						
Power Dissipation	P _{TOT}	T _C = 25°C	350	W	Fig. 3					
Operating and Storage Temperature	T_j , T_{stg}		-55 to 175	°C						



Electrical Characteristics Values **Parameter Symbol Conditions** Unit Note Min. Тур. Max. $I_F = 30 A$, $T_i = 25$ °C 1.5 1.8 ٧ Diode Forward Voltage V_{F} Fig. 1 $I_F = 30 \text{ A}, T_j = 175^{\circ}\text{C}$ 1.9 $V_R = 1200 \text{ V, } T_i = 25^{\circ}\text{C}$ 2 20 **Reverse Current** Fig. 2 I_R μΑ $V_R = 1200 \text{ V}, T_j = 175^{\circ}\text{C}$ 20 $V_R = 400 V$ 67 **Total Capacitive Charge** $Q_{\mathbb{C}}$ nC Fig. 7 $V_R = 800 V$ 97 $I_F \leq I_{F,MAX}$ $dI_F/dt = 200 A/\mu s$ V_R = 400 V Switching Time < 10 ts ns $V_R = 800 V$ $V_R = 1 V$, f = 1MHz1101 С **Total Capacitance** рF Fig. 6 V_R = 800 V, f = 1MHz 64

Thermal/Package Characteristics											
Parameter	Symbol	Conditions		Values			Note				
		Conditions	Min.	Тур.	Max.	Unit	Note				
Thermal Resistance, Junction - Case	RthJC			0.43		°C/W	Fig. 9				
Weight	W _T			1.45		g					



Figure 1: Typical Forward Characteristics

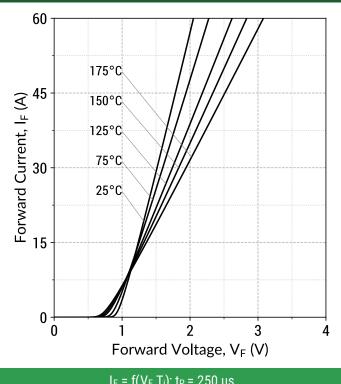
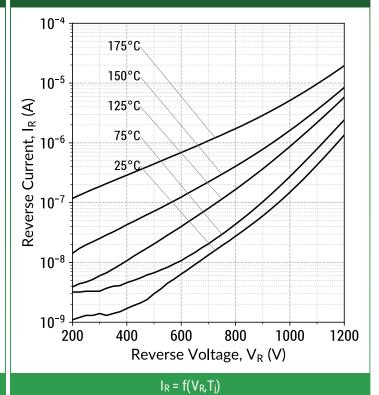


Figure 2: Typical Reverse Characteristics



 $I_F = f(V_F, T_j); t_P = 250 \mu s$

Figure 3: Power Derating Curves

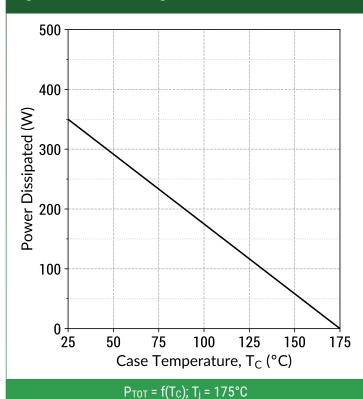
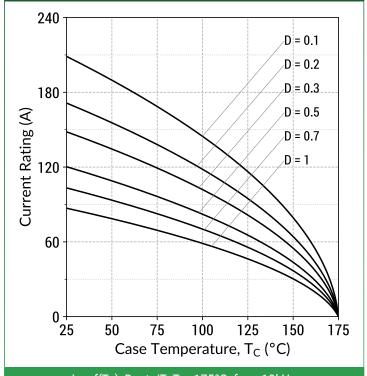


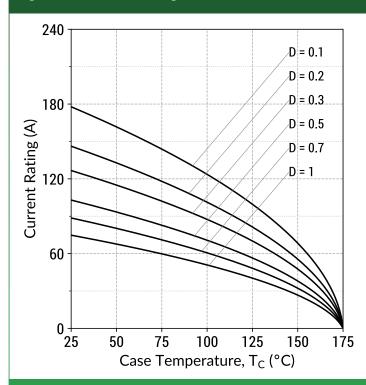
Figure 4: Current Derating Curves (Typical V_F)



 $I_F = f(T_C); D = t_P/T; T_j \le 175^{\circ}C; f_{SW} > 10kHz$

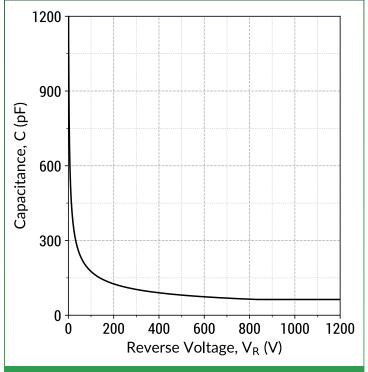


Figure 5: Current Derating Curves (Maximum V_F)



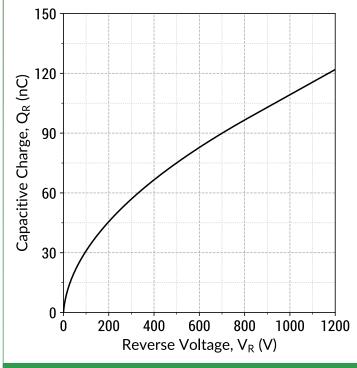
 $I_F = f(T_C); D = t_P/T; T_j \le 175$ °C; $f_{SW} > 10$ kHz

Figure 6: Typical Junction Capacitance vs Reverse Voltage Characteristics



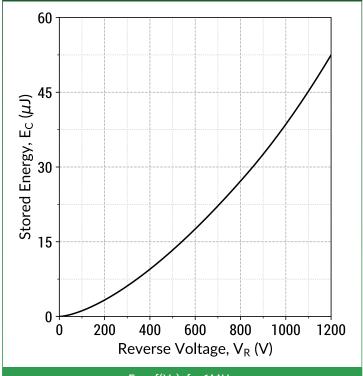
 $C = f(V_R)$; f = 1MHz

Figure 7: Typical Capacitive Charge vs Reverse Voltage Characteristics



 $Q_C = f(V_R)$; f = 1MHz

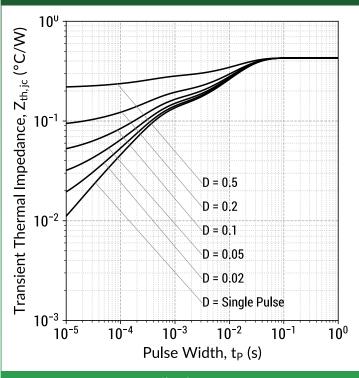
Figure 8: Typical Capacitive Energy vs Reverse Voltage Characteristics



 $E_C = f(V_R)$; f = 1MHz

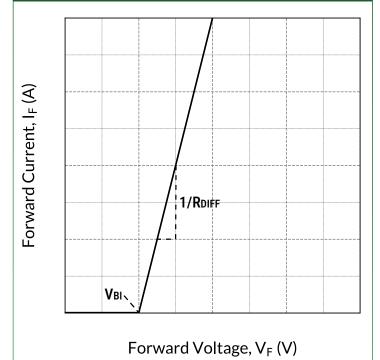


Figure 9: Transient Thermal Impedance



 $Z_{th,jc} = f(t_P,D); D = t_P/T$

Figure 10: Forward Curve Model



 $I_F = f(V_F, T_j)$

Forward Curve Model Equation:

 $I_F = (V_F - V_{BI})/R_{DIFF} (A)$

Built-In Voltage (V_{BI}):

 $V_{BI}(T_j) = m \times T_j + n (V)$ $m = -0.00119 (V/^{\circ}C)$ n = 1.01 (V)

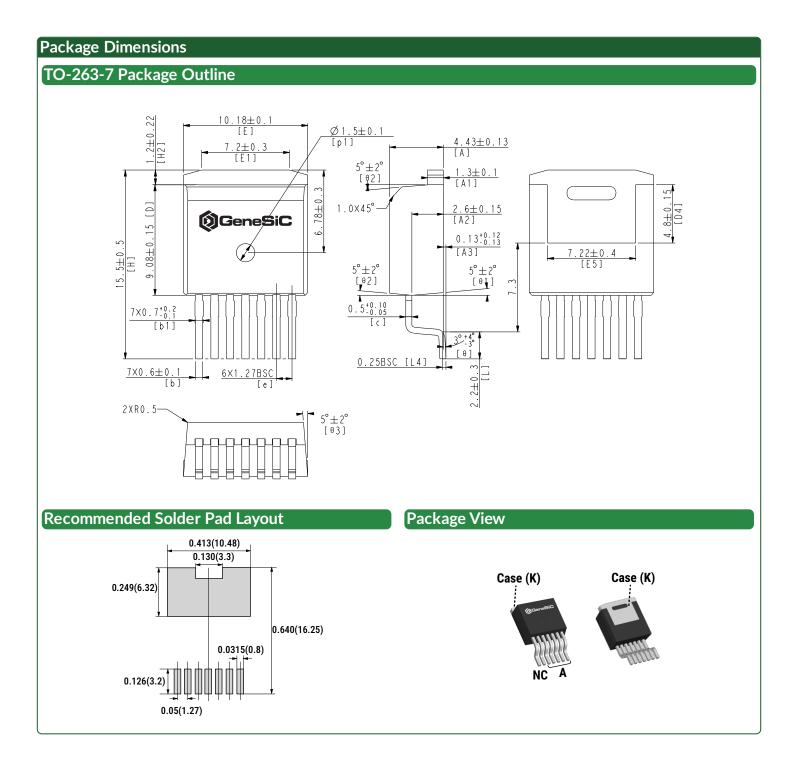
Differential Resistance (RDIFF):

 $R_{DIFF}(T_j) = a \times T_j^2 + b \times T_j + c (\Omega)$ $a = 3.97e-07 (\Omega/^{\circ}C^2)$ $b = 5.5e-05 (\Omega/^{\circ}C)$ $c = 0.0163 (\Omega)$

Forward Power Loss Equation:

 $P_{LOSS} = V_{BI}(T_j) \times I_{AVG} + R_{DIFF}(T_j) \times I_{RMS}^2$





NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.





Compliance

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

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Quality Manual: https://www.genesicsemi.com/quality

Revision History

Rev 21/Jul: Initial Release



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