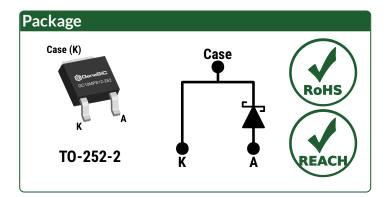
Silicon Carbide Schottky Diode



 V_{RRM} = 1200 V $I_{F(T_C = 162^{\circ}C)}$ = 10 A Q_C = 53 nC

Features

- Low V_F for High Temperature Operation
- Enhanced Surge and Avalanche Robustness
- Superior Figure of Merit Qc/IF
- Low Thermal Resistance
- Low Reverse Leakage Current
- Temperature Independent Fast Switching
- Positive Temperature Coefficient of V_F
- High dV/dt Ruggedness



Advantages

- Improved System Efficiency
- High System Reliability
- Optimal Price Performance
- Reduced Cooling Requirements
- Increased System Power Density
- Zero Reverse Recovery Current
- Easy to Parallel without Thermal Runaway
- Enables Extremely Fast Switching

Applications

- Power Factor Correction (PFC)
- Solar Inverters
- Battery Chargers
- High Frequency Converters
- Switched Mode Power Supply (SMPS)
- AC/DC Power Supplies
- Anti-Parallel / Free-Wheeling Diode
- LED and HID Lighting

Absolute Maximum Ratings (At T _C = 25°C Unless Otherwise Stated)								
Parameter	Symbol	Conditions	Values	Unit	Note			
Repetitive Peak Reverse Voltage	V_{RRM}		1200	٧				
Continuous Forward Current	l _F	$T_C = 100^{\circ}C, D = 1$	28					
		$T_C = 135^{\circ}C, D = 1$	19	Α	Fig. 4			
		$T_C = 162^{\circ}C$, D = 1	10					
Non-Repetitive Peak Forward Surge Current, Half Sine Wave	Іғ,ѕм	$T_C = 25^{\circ}C$, $t_P = 10 \text{ ms}$	100	Α				
		T_C = 150°C, t_P = 10 ms	80					
Repetitive Peak Forward Surge Current, Half Sine Wave	I _{F,RM}	T_C = 25°C, t_P = 10 ms	60	۸				
		T_C = 150°C, t_P = 10 ms	42	Α				
Non-Repetitive Peak Forward Surge Current	I _{F,MAX}	T_C = 25°C, t_P = 10 μ s	500	Α				
i ² t Value	∫i²dt	$T_C = 25^{\circ}C$, $t_P = 10 \text{ ms}$	50	A ² s				
Non-Repetitive Avalanche Energy	E _{AS}	L = 3.6 mH, I _{AS} = 10 A	180	mJ				
Diode Ruggedness	dV/dt	V _R = 0 ~ 960 V	200	V/ns				
Power Dissipation	P _{TOT}	T _C = 25°C	218	W	Fig. 3			
Operating and Storage Temperature	T_j , T_{stg}		-55 to 175	°C				



Electrical Characteristics								
Parameter	Symbol	Conditions		Values			Unit	Note
	Зушьог			Min.	Тур.	Max.	Oilit	Note
Diode Forward Voltage	V_F	I _F = 10 A, T _j = 25°C			1.5	1.8	٧	Fig. 1
	VF	$I_F = 10 \text{ A, } T_j = 175^{\circ}\text{C}$			1.9			
Reverse Current	ln.	$V_R = 1200 \text{ V, } T_j = 25^{\circ}\text{C}$			1	5	۸	Fig. 2
	IR	$V_R = 1200 \text{ V, } T_j = 175^{\circ}\text{C}$			11		μA	
Total Capacitive Charge	Qc		$V_R = 400 V$		37		nC	Fig. 7
	QU	$- \int_{-1}^{1} I_{F,MAX} dI_{F}/dt = 200 A/\mu s$	$V_R = 800 V$		53		110	
Switching Time	ts		V_R = 400 V		< 10		ns	
	ıs		$V_{R} = 800 V$		\ 10		119	
Total Capacitance C	C	$V_R = 1 V, f = 1 V$	= 1MHz		609		nЕ	Fig. 6
		V _R = 800 V, f = 1MHz			36		pF	

Thermal/Package Characteristics									
Symbol	Conditions		Values			Mata			
	Conditions	Min.	Тур.	Max.	Unit	Note			
RthJC			0.69		°C/W	Fig. 9			
W _T			0.3		g				
	Symbol R _{thJC}	Symbol Conditions	Symbol Conditions Min.	$\begin{tabular}{c} Symbol & Conditions & \hline & Values \\ \hline Min. & Typ. \\ R_{thJC} & 0.69 \\ \hline \end{tabular}$	Symbol Conditions Values Min. Typ. Max.	$\begin{tabular}{lllllllllllllllllllllllllllllllllll$			





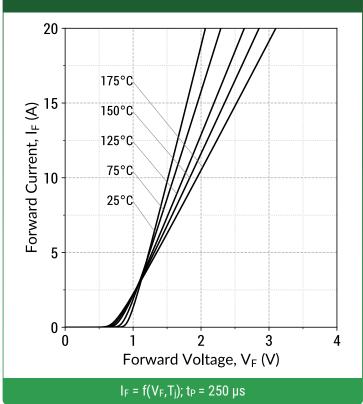


Figure 2: Typical Reverse Characteristics

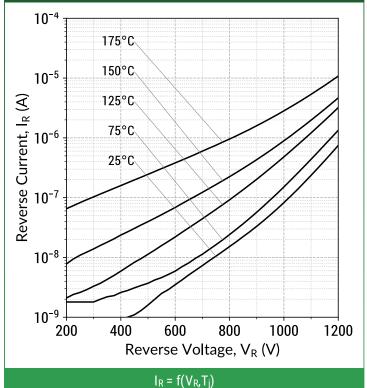


Figure 3: Power Derating Curves

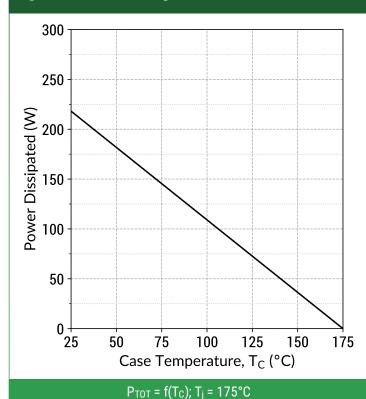
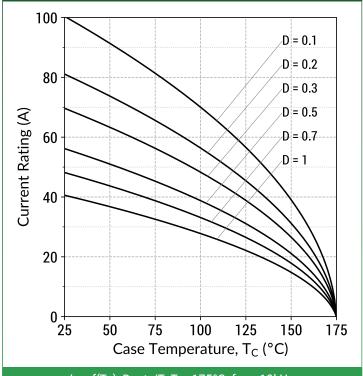


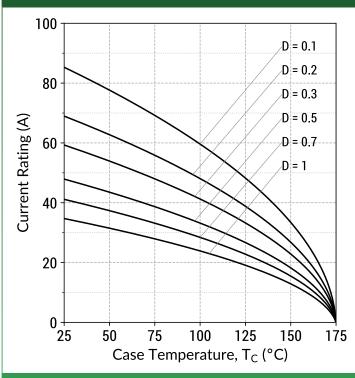
Figure 4: Current Derating Curves (Typical V_F)



 $I_F = f(T_C)$; D = t_P/T ; $T_j \le 175$ °C; $f_{SW} > 10$ kHz

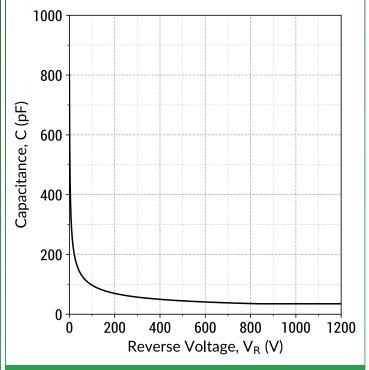


Figure 5: Current Derating Curves (Maximum V_F)



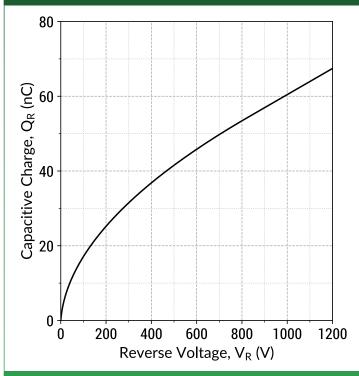
 $I_F = f(T_C)$; D = t_P/T ; $T_j \le 175$ °C; $f_{SW} > 10$ kHz

Figure 6: Typical Junction Capacitance vs Reverse Voltage Characteristics



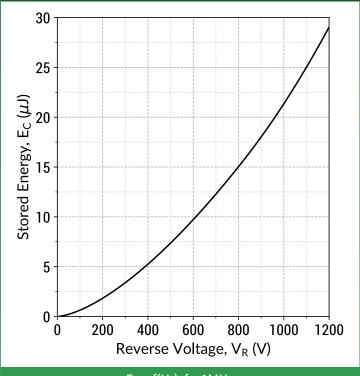
 $C = f(V_R)$; f = 1MHz

Figure 7: Typical Capacitive Charge vs Reverse Voltage Characteristics



 $Q_C = f(V_R)$; f = 1MHz

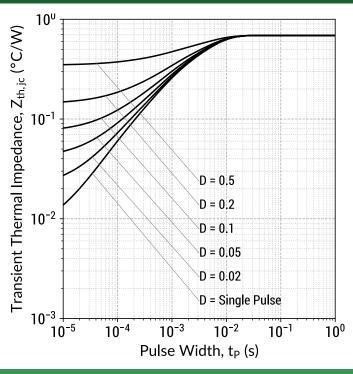
Figure 8: Typical Capacitive Energy vs Reverse Voltage Characteristics



 $E_C = f(V_R)$; f = 1MHz

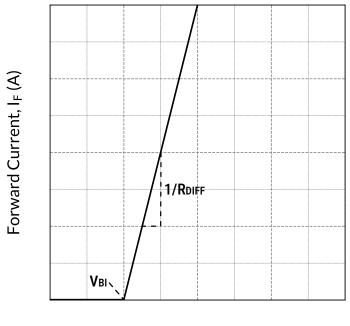


Figure 9: Transient Thermal Impedance



 $Z_{th,jc} = f(t_P,D); D = t_P/T$

Figure 10: Forward Curve Model



Forward Voltage, $V_F(V)$

 $I_F = f(V_F, T_j)$

Forward Curve Model Equation:

 $I_F = (V_F - V_{BI})/R_{DIFF} (A)$

Built-In Voltage (V_{BI}):

$$V_{BI}(T_j) = m \times T_j + n (V)$$

 $m = -0.00123 (V/^{\circ}C)$
 $n = 0.995 (V)$

Differential Resistance (RDIFF):

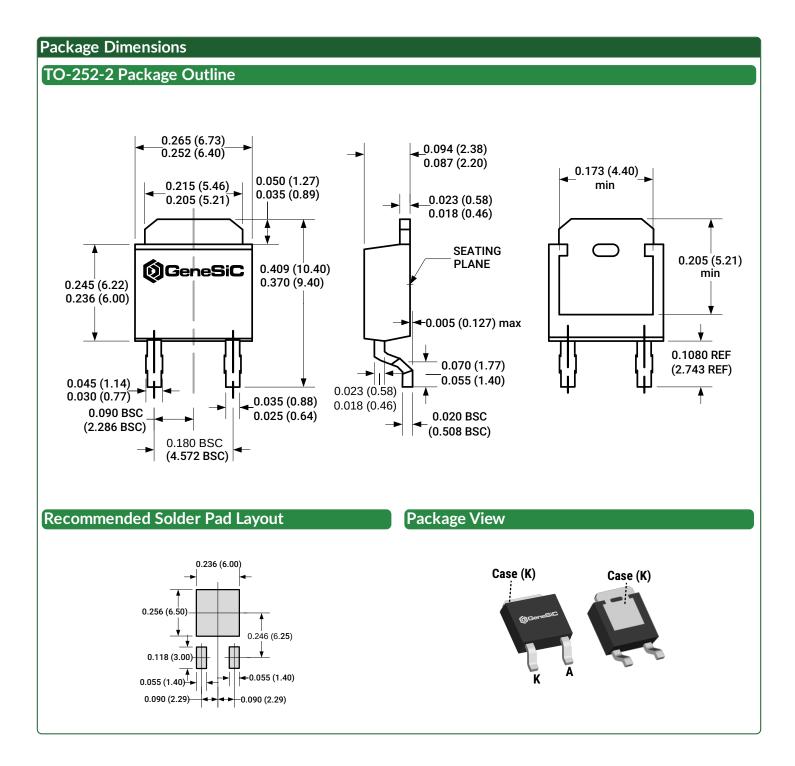
$$R_{DIFF}(T_j) = a \times T_j^2 + b \times T_j + c (\Omega)$$

 $a = 1.19e-06 (\Omega/^{\circ}C^2)$
 $b = 0.000169 (\Omega/^{\circ}C)$
 $c = 0.0502 (\Omega)$

Forward Power Loss Equation:

$$P_{LOSS} = V_{BI}(T_j) \times I_{AVG} + R_{DIFF}(T_j) \times I_{RMS}^2$$





NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.





Compliance

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

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Related Links

SPICE Models: https://www.genesicsemi.com/sic-schottky-mps/GC10MPS12-252/GC10MPS12-252_SPICE.zip
 PLECS Models: https://www.genesicsemi.com/sic-schottky-mps/GC10MPS12-252/GC10MPS12-252_PLECS.zip
 CAD Models: https://www.genesicsemi.com/sic-schottky-mps/GC10MPS12-252/GC10MPS12-252_3D.zip

• Evaluation Boards: https://www.genesicsemi.com/technical-support

Reliability: https://www.genesicsemi.com/reliability
 Compliance: https://www.genesicsemi.com/compliance
 Quality Manual: https://www.genesicsemi.com/quality

Revision History

Rev 21/Jul: Updated with most recent test data
 Supersedes: Rev 19/Apr, Rev 20/Apr, Rev 20/Apr



www.genesicsemi.com/sic-schottky-mps/

