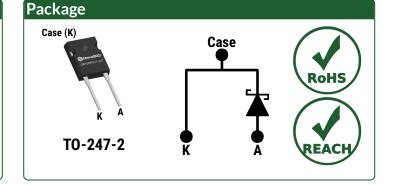
Silicon Carbide Schottky Diode



V <sub>RRM</sub> =	1700 V
I <sub>F(Tc</sub> = 163°C) =	10 A
Q <sub>c</sub> =	108 nC

#### Features

- Low V<sub>F</sub> for High Temperature Operation
- Enhanced Surge and Avalanche Robustness
- Superior Figure of Merit Q<sub>C</sub>/I<sub>F</sub>
- Low Thermal Resistance
- Low Reverse Leakage Current
- Temperature Independent Fast Switching
- Positive Temperature Coefficient of V<sub>F</sub>
- Low V<sub>F</sub> for High Temperature Operation



### Advantages

- Improved System Efficiency
- High System Reliability
- Optimal Price Performance
- Reduced Cooling Requirements
- Increased System Power Density
- Zero Reverse Recovery Current
- Easy to Parallel without Thermal Runaway
- Improved System Efficiency

#### Applications

- EV Fast Chargers
- Solar Inverters
- Anti-Parallel / Free-Wheeling Diode
- Motor Drives
- High Frequency Rectifiers
- Switched Mode Power Supply (SMPS)
- Induction Heating and Welding
- Pulsed Power

### Absolute Maximum Ratings (At T<sub>c</sub> = 25°C Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit	Note
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>		1700	V	
		T <sub>C</sub> = 100°C, D = 1	29		
Continuous Forward Current	IF	T <sub>C</sub> = 135°C, D = 1	20	Α	Fig. 4
		T <sub>C</sub> = 163°C, D = 1	10		
Non-Repetitive Peak Forward Surge Current, Half Sine	I	T <sub>C</sub> = 25°C, t <sub>P</sub> = 10 ms	108	٨	
Wave	IF,SM	T <sub>C</sub> = 150°C, t <sub>P</sub> = 10 ms	86	A	
Departitive Deals Forward Surge Current Half Sine Ways	I	T <sub>C</sub> = 25°C, t <sub>P</sub> = 10 ms	64	٨	
Repetitive Peak Forward Surge Current, Half Sine Wave	I <sub>F,RM</sub>	T <sub>C</sub> = 150°C, t <sub>P</sub> = 10 ms	45	А	
Non-Repetitive Peak Forward Surge Current	I <sub>F,MAX</sub>	T <sub>C</sub> = 25°C, t <sub>P</sub> = 10 μs	540	Α	
i²t Value	∫i²dt	T <sub>C</sub> = 25°C, t <sub>P</sub> = 10 ms	58	A <sup>2</sup> s	
Non-Repetitive Avalanche Energy	E <sub>AS</sub>	L = 5.2 mH, I <sub>AS</sub> = 10 A	261	mJ	
Diode Ruggedness	dV/dt	V <sub>R</sub> = 0 ~ 1360 V	200	V/ns	
Power Dissipation	Ртот	T <sub>C</sub> = 25°C	274	W	Fig. 3
Operating and Storage Temperature	Tj, Tstg		-55 to 175	°C	



## **Electrical Characteristics**

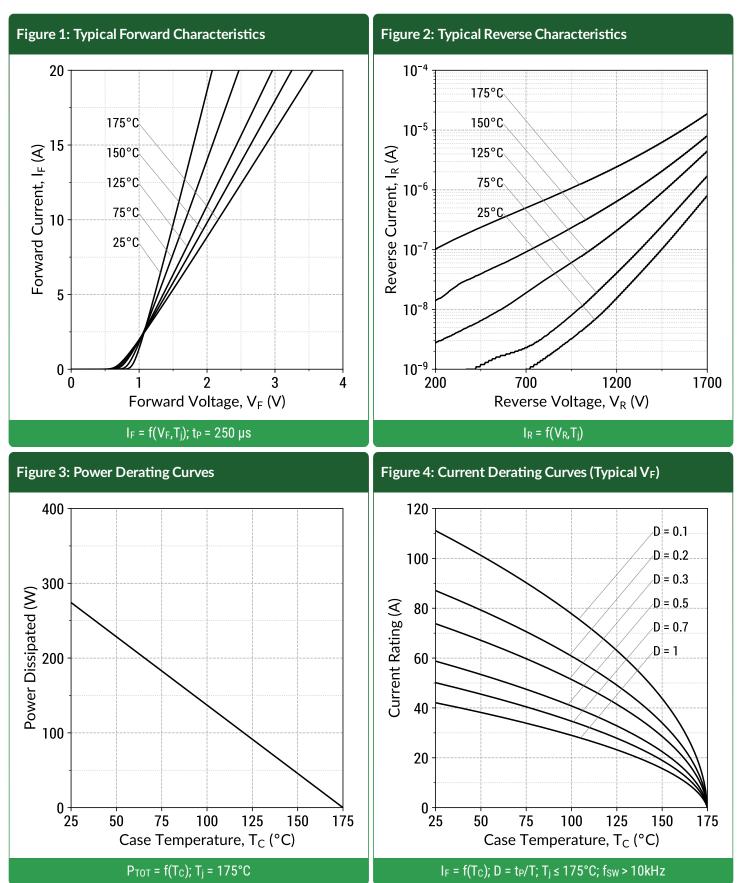
Parameter	Symbol	Conditions		Values			Unit	Note
Falallelel	Symbol			Min.	Тур.	Max.	UIII	Note
Diode Forward Voltage	VF	I <sub>F</sub> = 10 A, T <sub>j</sub> = 25°C			1.5	1.8	۷	Fig. 1
	VF	I <sub>F</sub> = 10 A, T <sub>j</sub> = 175°C			2.1			
Reverse Current	la la	V <sub>R</sub> = 1700 V, T <sub>j</sub> = 25°C			1	10		Fig. 2
	IR	V <sub>R</sub> = 1700 V, T <sub>j</sub> = 175°C			9		μA	
Total Capacitive Charge	0-	V <sub>R</sub> = 600 V			74		nC	Fig. 7
	Qc	I <sub>F</sub> ≤ I <sub>F,MAX</sub>	V <sub>R</sub> = 1200 V		108		nc	Fig. 7
Switching Time	+-	dl <sub>F</sub> /dt = 200 A/µs V <sub>R</sub> = 600 V			< 10			
	ts		V <sub>R</sub> = 1200 V		< 10		ns	
Total Capacitance	0	V <sub>R</sub> = 1 V, f = 1MHz			940		<b>"Г</b>	Fig. 6
	С	V <sub>R</sub> = 1200 V, f = 1MHz			52		pF	

## Thermal/Package Characteristics

Deremeter	Symbol	Conditions	Values			l lasta	Noto
Parameter		Conditions	Min.	Тур.	Max.	Unit	Note
Thermal Resistance, Junction - Case	RthJC			0.55		°C/W	Fig. 9
Weight	WT			6.0		g	
Mounting Torque	Тм	Screws to Heatsink			1.1	Nm	

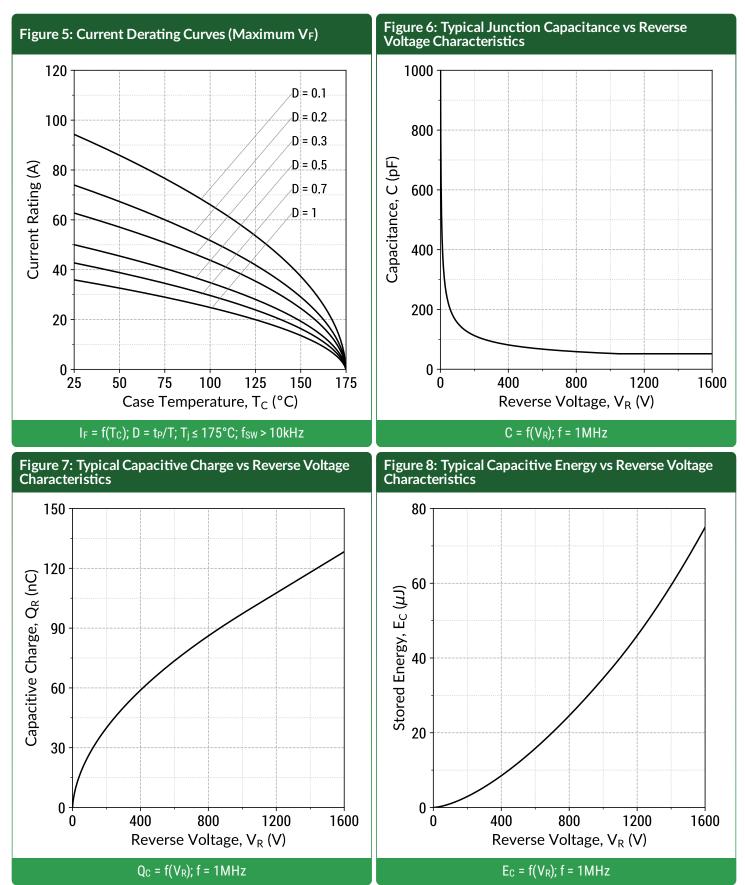






Rev 21/Jun





Rev 21/Jun

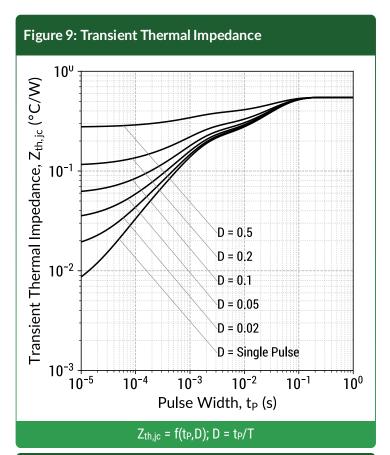


Figure 10: Forward Curve Model

 $I_F = f(V_F, T_j)$ 

### Forward Curve Model Equation:

 $I_{F} = (V_{F} - V_{BI})/R_{DIFF} (A)$ 

Built-In Voltage (V<sub>BI</sub>):

 $V_{BI}(T_j) = m \times T_j + n (V)$ m = -0.00128 (V/°C) n = 0.99 (V)

Differential Resistance (RDIFF):

 $R_{DIFF}(T_j) = a \times T_j^2 + b \times T_j + c (\Omega)$ a = 1.01e-06 (\Omega/°C^2) b = 0.000355 (\Omega/°C) c = 0.0465 (\Omega)

Forward Power Loss Equation:

 $P_{LOSS} = V_{BI}(T_j) \times I_{AVG} + R_{DIFF}(T_j) \times I_{RMS}^2$ 

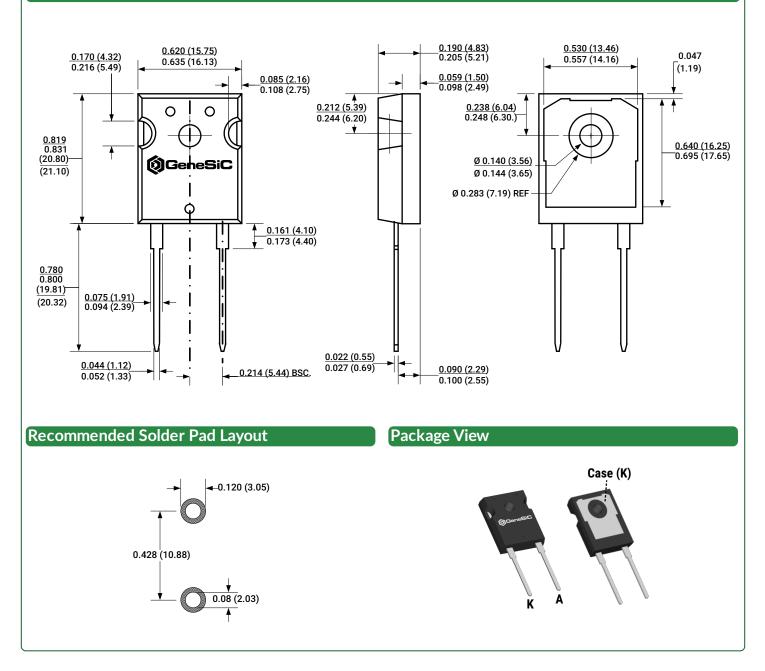


Rev 21/Jun



### Package Dimensions

### TO-247-2 Package Outline



#### NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.

# GB10MPS17-247 1700V 10A SiC Schottky MPS™ Diode



### Compliance

#### **RoHS Compliance**

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

#### **REACH Compliance**

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

### Disclaimer

GeneSiC Semiconductor, Inc. reserves right to make changes to the product specifications and data in this document without notice. GeneSiC disclaims all and any warranty and liability arising out of use or application of any product. No license, express or implied to any intellectual property rights is granted by this document.

Unless otherwise expressly indicated, GeneSiC products are not designed, tested or authorized for use in life-saving, medical, aircraft navigation, communication, air traffic control and weapons systems, nor in applications where their failure may result in death, personal injury and/or property damage.

### **Related Links**

SPICE Models:	https://www.genesicsemi.com/sic-schottky-mps/GB10MPS17-247/GB10MPS17-247_SPICE.zip
• PLECS Models:	https://www.genesicsemi.com/sic-schottky-mps/GB10MPS17-247/GB10MPS17-247_PLECS.zip
CAD Models:	https://www.genesicsemi.com/sic-schottky-mps/GB10MPS17-247/GB10MPS17-247_3D.zip
Evaluation Boards	: https://www.genesicsemi.com/technical-support
Reliability:	https://www.genesicsemi.com/reliability
Compliance:	https://www.genesicsemi.com/compliance
• Quality Manual:	https://www.genesicsemi.com/quality

### **Revision History**

- Rev 21/Jun: Updated with most recent test data
- Supersedes: Rev 19/Apr, Rev 20/Apr, Rev 20/Aug



## www.genesicsemi.com/sic-schottky-mps/



Rev 21/Jun Copyright© 2021 GeneSiC Semiconductor Inc. All Rights Reserved.