

# G3R30MT12-CAL

## 1200 V 30 mΩ SiC MOSFET



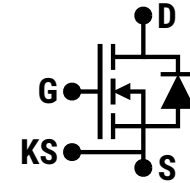
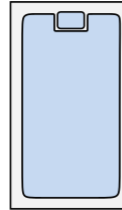
Silicon Carbide MOSFET  
N-Channel Enhancement Mode

V <sub>DS</sub>	=	1200 V
R <sub>DS(ON)(Typ.)</sub>	=	30 mΩ
I <sub>D(Tc = 100°C)</sub>	=	63 A

### Features

- G3R™ Technology with +15 V Gate Drive
- Softer R<sub>DS(ON)</sub> v/s Temperature Dependency
- LoRing™ - Electromagnetically Optimized Design
- Smaller R<sub>G(INT)</sub> and Lower Q<sub>G</sub>
- Low Device Capacitances (C<sub>OSS</sub>, C<sub>RSS</sub>)
- Superior Cost-Performance Index
- Robust Body Diode with Low V<sub>F</sub> and Low Q<sub>RR</sub>
- Industry-Leading UIL & Short-Circuit Robustness

### Bare Chip



D = Drain  
G = Gate  
S = Source  
KS = Kelvin Source



### Advantages

- Compatible with Commercial Gate Drivers
- Low Conduction Losses at all Temperatures
- Reduced Ringing
- Faster and More Efficient Switching
- Lesser Switching Spikes and Lower Losses
- Better Power Density and System Efficiency
- Ease of Paralleling without Thermal Runaway
- Higher System Reliability

### Applications

- Solar Inverters
- Motor Drives
- EV Charging
- High Voltage DC-DC Converters
- Switched Mode Power Supplies
- UPS
- Smart Grid Transmission and Distribution
- Induction Heating and Welding

### Absolute Maximum Ratings (At T<sub>C</sub> = 25°C Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit	Note
Drain-Source Voltage	V <sub>DS(max)</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 100 μA	1200	V	
Gate-Source Voltage (Dynamic)	V <sub>GS(max)</sub>		-10 / +20	V	
Gate-Source Voltage (Static)	V <sub>GS(op)</sub>	Recommended Operation	-5 / +15	V	
Continuous Forward Current	I <sub>D</sub>	T <sub>C</sub> = 25°C, V <sub>GS</sub> = -5 / +15 V	83	A	
		T <sub>C</sub> = 100°C, V <sub>GS</sub> = -5 / +15 V	63		
		T <sub>C</sub> = 135°C, V <sub>GS</sub> = -5 / +15 V	51		
Pulsed Drain Current	I <sub>D(pulse)</sub>	t <sub>p</sub> ≤ 3 μs, D ≤ 1%, V <sub>GS</sub> = 15 V, Note 1	200	A	
Power Dissipation	P <sub>D</sub>	T <sub>C</sub> = 25°C	420	W	Note 2
Non-Repetitive Avalanche Energy	E <sub>AS</sub>	L = 2.0 mH, I <sub>AS</sub> = 22.5 A	498	mJ	
Operating and Storage Temperature	T <sub>j</sub> , T <sub>stg</sub>		-55 to 200	°C	

Note 1: Pulse Width t<sub>p</sub> Limited by T<sub>j(max)</sub>

Electrical Characteristics (At  $T_C = 25^\circ\text{C}$  Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values			Unit	Note
			Min.	Typ.	Max.		
Drain-Source Breakdown Voltage	$V_{DSS}$	$V_{GS} = 0\text{ V}, I_D = 100\ \mu\text{A}$	1200			V	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 1200\text{ V}, V_{GS} = 0\text{ V}$		1		$\mu\text{A}$	
Gate Source Leakage Current	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = 20\text{ V}$			100	nA	
		$V_{DS} = 0\text{ V}, V_{GS} = -10\text{ V}$			-100		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 24.0\text{ mA}$	1.8	2.70		V	Fig. 9
		$V_{DS} = V_{GS}, I_D = 24.0\text{ mA}, T_j = 200^\circ\text{C}$		2.00			
Transconductance	$g_{fs}$	$V_{DS} = 10\text{ V}, I_D = 45\text{ A}$		21.0		S	Fig. 4
		$V_{DS} = 10\text{ V}, I_D = 45\text{ A}, T_j = 200^\circ\text{C}$		24.0			
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 15\text{ V}, I_D = 45\text{ A}$		30	39	mΩ	Fig. 5-8
		$V_{GS} = 15\text{ V}, I_D = 45\text{ A}, T_j = 200^\circ\text{C}$		46			
Input Capacitance	$C_{iss}$			3863			
Output Capacitance	$C_{oss}$			117		pF	Fig. 11
Reverse Transfer Capacitance	$C_{rss}$			9.4			
$C_{oss}$ Stored Energy	$E_{oss}$	$V_{DS} = 800\text{ V}, V_{GS} = 0\text{ V}$ $f = 1\text{ MHz}, V_{AC} = 25\text{ mV}$		45		$\mu\text{J}$	Fig. 12
$C_{oss}$ Stored Charge	$Q_{oss}$			170		nC	
Effective Output Capacitance (Energy Related)	$C_{o(er)}$			140		pF	Note 3
Effective Output Capacitance (Time Related)	$C_{o(tr)}$			212			
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 800\text{ V}, V_{GS} = -5/+15\text{ V}$		43		nC	Fig. 10
Gate-Drain Charge	$Q_{gd}$	$I_D = 45\text{ A}$		51			
Total Gate Charge	$Q_g$	Per IEC607478-4		118			
Internal Gate Resistance	$R_{G(int)}$	$f = 1\text{ MHz}, V_{AC} = 25\text{ mV}$		1.2		$\Omega$	
Turn-On Switching Energy (Body Diode)	$E_{on}$	$T_j = 25^\circ\text{C}; V_{GS} = -5/+15\text{V}; R_{G(ext)} = 3\ \Omega, I_D = 45\text{ A}; V_{DD} = 800\text{ V}$		156		$\mu\text{J}$	Fig. 18
Turn-Off Switching Energy (Body Diode)	$E_{off}$			71			
Turn-On Delay Time	$t_{d(on)}$			13		ns	Fig. 20
Rise Time	$t_r$	$V_{DD} = 800\text{ V}, V_{GS} = -5/+15\text{V}$ $R_{G(ext)} = 3\ \Omega, I_D = 45\text{ A}$		11			
Turn-Off Delay Time	$t_{d(off)}$	Timing relative to $V_{DS}$ , Resistive load		9			
Fall Time	$t_f$			12			

\*The chip technology was characterized up to 200 V/ns. The measured  $dV/dt$  was limited by measurement test setup and package.

Note 2: Assuming  $R_{thJC(max)} = 0.42^\circ\text{C/W}$

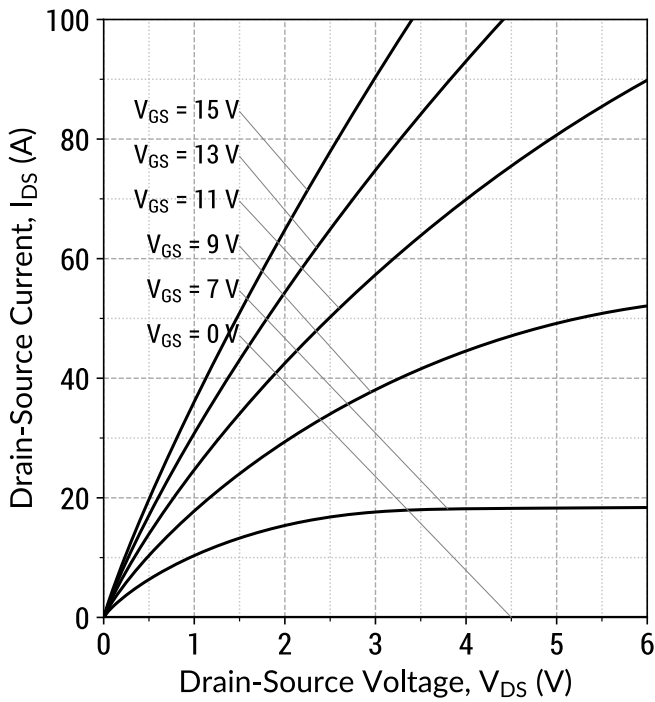
Note 3:  $C_{o(er)}$ , a lumped capacitance that gives same stored energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 800V.

$C_{o(tr)}$ , a lumped capacitance that gives same charging times as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 800V.

### Reverse Diode Characteristics

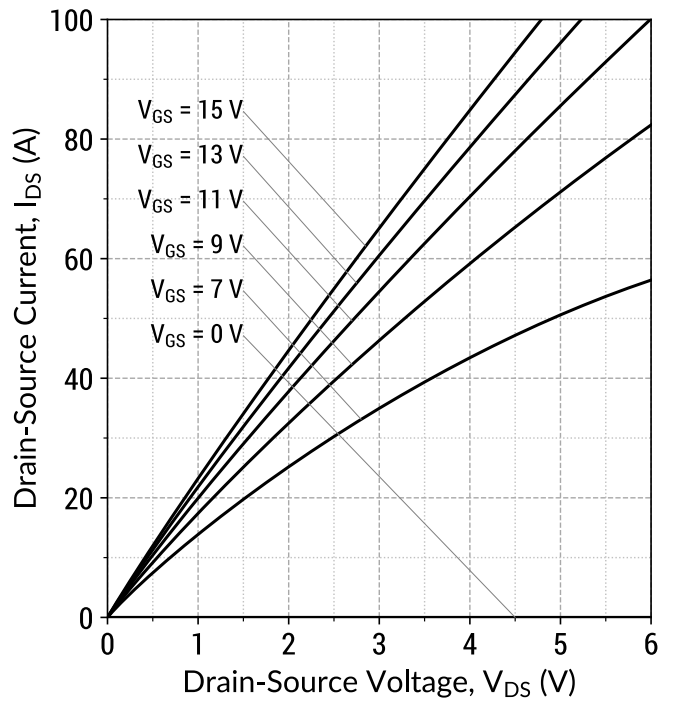
Parameter	Symbol	Conditions	Values			Unit	Note
			Min.	Typ.	Max.		
Diode Forward Voltage	$V_{SD}$	$V_{GS} = -5\text{ V}, I_{SD} = 22\text{ A}$ $V_{GS} = -5\text{ V}, I_{SD} = 22\text{ A}, T_j = 200^\circ\text{C}$		4.7 4.3		V	Fig. 13-14
Continuous Diode Forward Current	$I_S$	$V_{GS} = -5\text{ V}, T_c = 100^\circ\text{C}$	42			A	
Diode Pulse Current	$I_{S(pulse)}$	$V_{GS} = -5\text{ V}, \text{Note 1}$		168		A	
Reverse Recovery Time	$t_{rr}$			24		ns	
Reverse Recovery Charge	$Q_{rr}$	$V_{GS} = -5\text{ V}, I_{SD} = 45\text{ A}, V_R = 800\text{ V}$ $dif/dt = 1000\text{ A}/\mu\text{s}, T_j = 25^\circ\text{C}$		159		nC	
Peak Reverse Recovery Current	$I_{rrm}$			6		A	
Reverse Recovery Time	$t_{rr}$			40		ns	
Reverse Recovery Charge	$Q_{rr}$	$V_{GS} = -5\text{ V}, I_{SD} = 45\text{ A}, V_R = 800\text{ V}$ $dif/dt = 1000\text{ A}/\mu\text{s}, T_j = 200^\circ\text{C}$		413		nC	
Peak Reverse Recovery Current	$I_{rrm}$			10		A	

Figure 1: Output Characteristics ( $T_j = 25^\circ\text{C}$ )



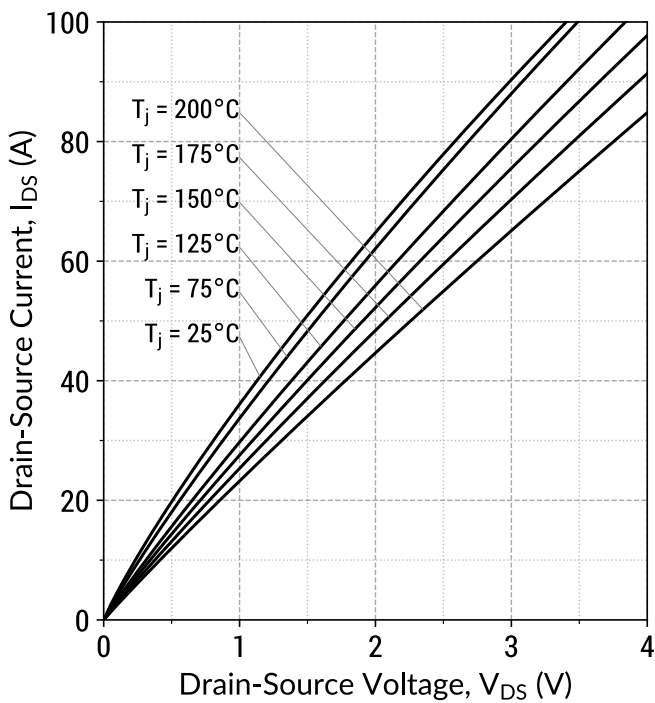
$$I_D = f(V_{DS}, V_{GS}); t_P = 250 \mu\text{s}$$

Figure 2: Output Characteristics ( $T_j = 200^\circ\text{C}$ )



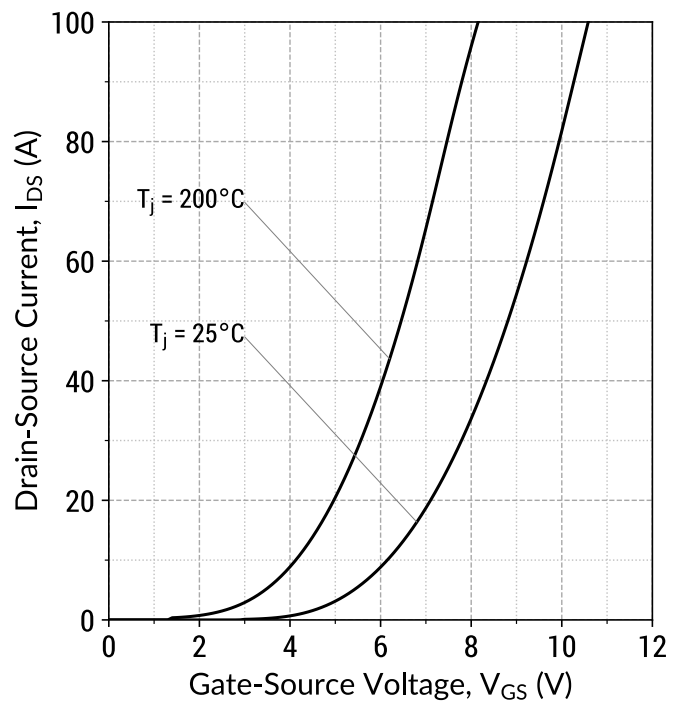
$$I_D = f(V_{DS}, V_{GS}); t_P = 250 \mu\text{s}$$

Figure 3: Output Characteristics ( $V_{GS} = 15 \text{ V}$ )



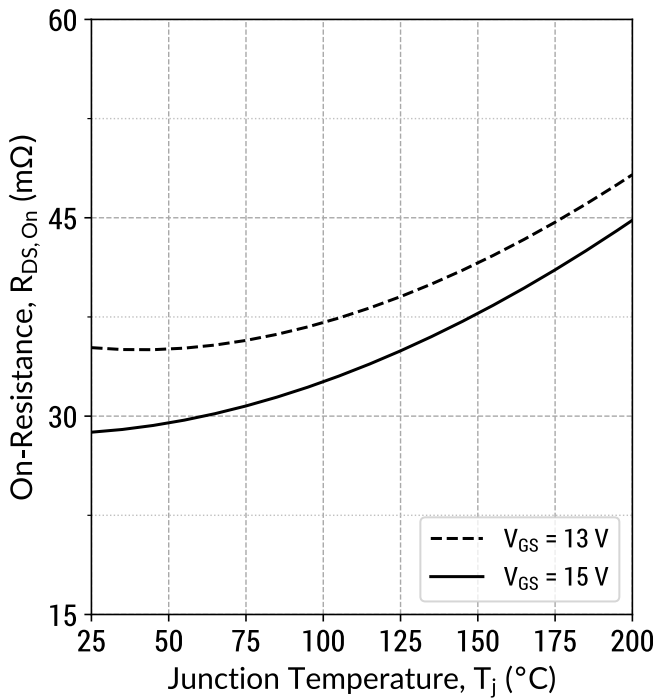
$$I_D = f(V_{DS}, T_j); t_P = 250 \mu\text{s}$$

Figure 4: Transfer Characteristics ( $V_{DS} = 10 \text{ V}$ )



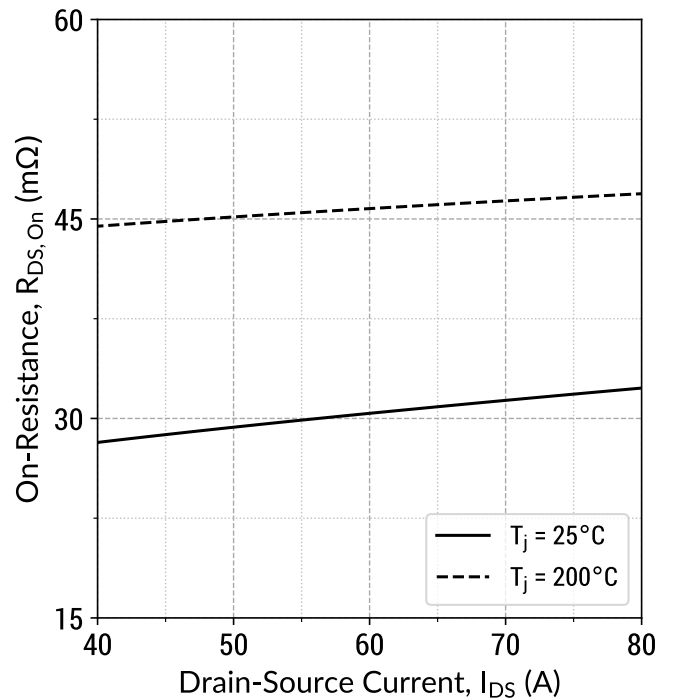
$$I_D = f(V_{GS}, T_j); t_P = 100 \mu\text{s}$$

Figure 5: On-State Resistance v/s Temperature



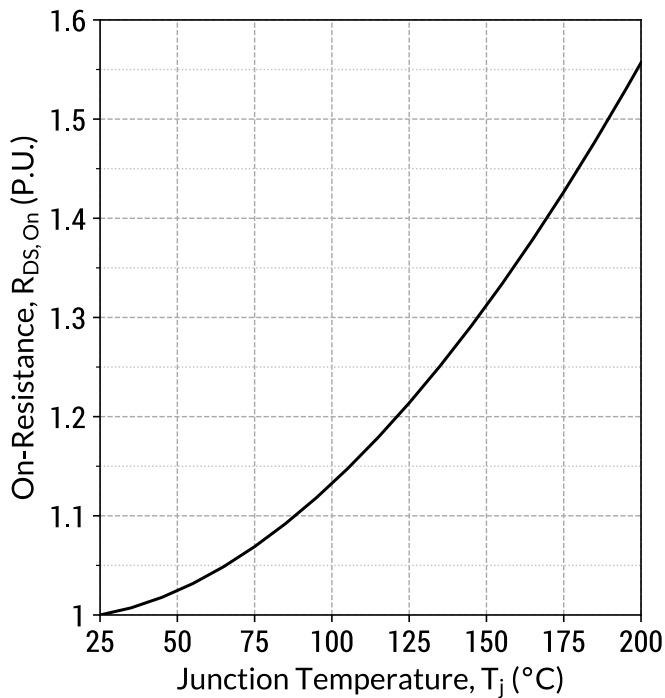
$R_{DS(on)} = f(T_j, V_{GS}); t_P = 250\ \mu\text{s}; I_D = 45\ \text{A}$

Figure 6: On-State Resistance v/s Drain Current



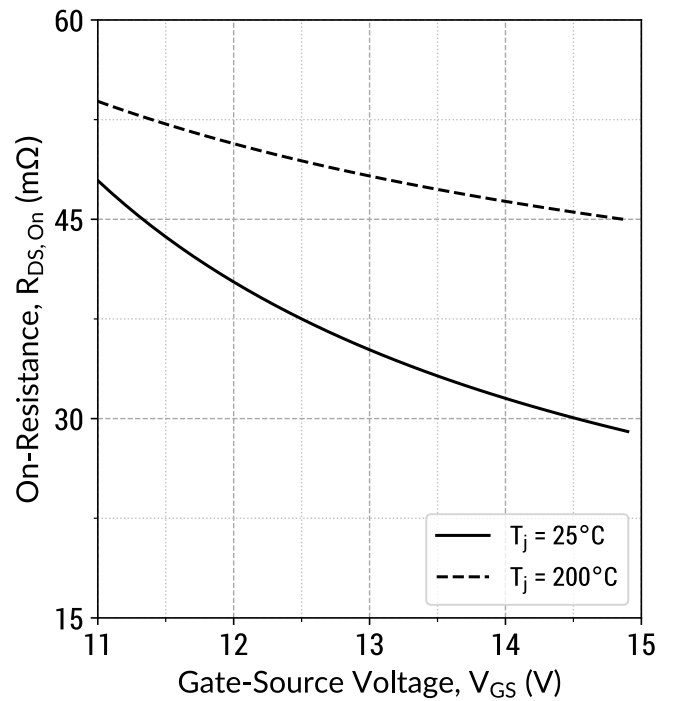
$R_{DS(on)} = f(T_j, I_D); t_P = 250\ \mu\text{s}; V_{GS} = 15\ \text{V}$

Figure 7: Normalized On-State Resistance v/s Temperature



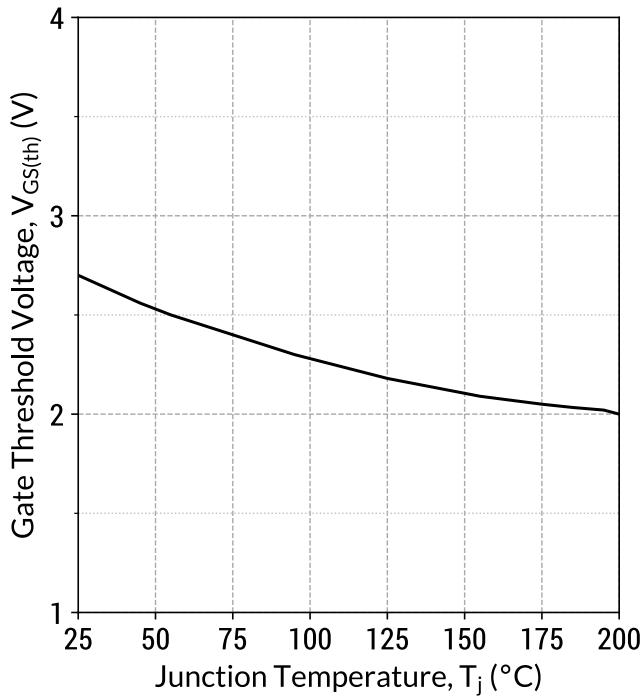
$R_{DS(on)} = f(T_j); t_P = 250\ \mu\text{s}; I_D = 45\ \text{A}; V_{GS} = 15\ \text{V}$

Figure 8: On-State Resistance v/s Gate Voltage



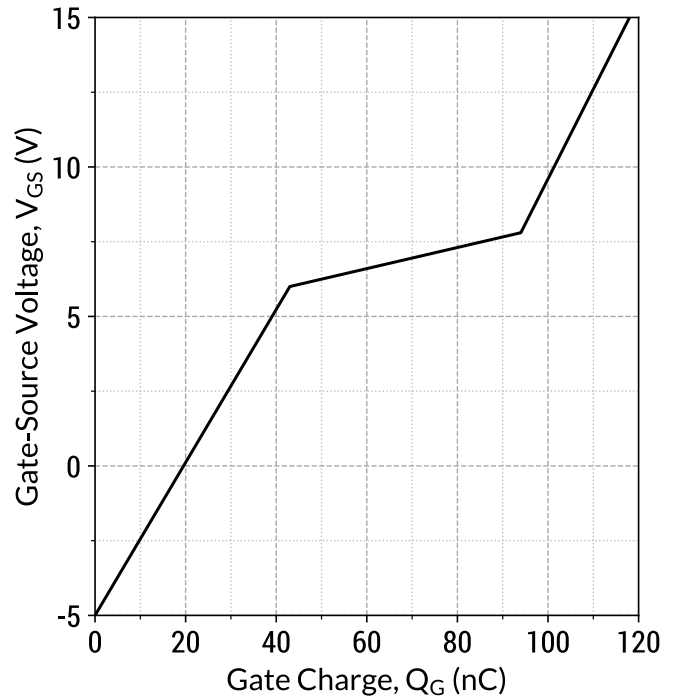
$R_{DS(on)} = f(T_j, V_{GS}); t_P = 250\ \mu\text{s}; I_D = 45\ \text{A}$

Figure 9: Threshold Voltage Characteristics



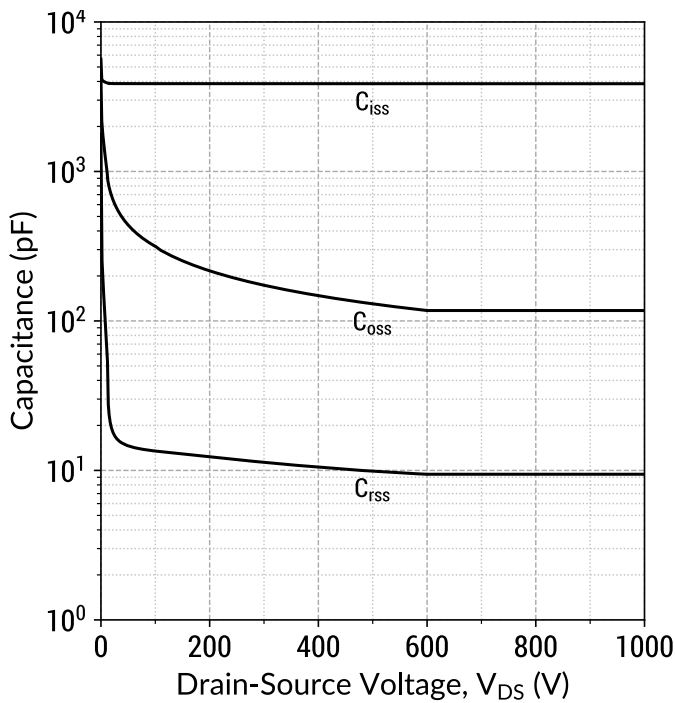
$V_{GS(th)} = f(T_j); V_{DS} = V_{GS}; I_D = 24.0 \text{ mA}$

Figure 10: Gate Charge Characteristics



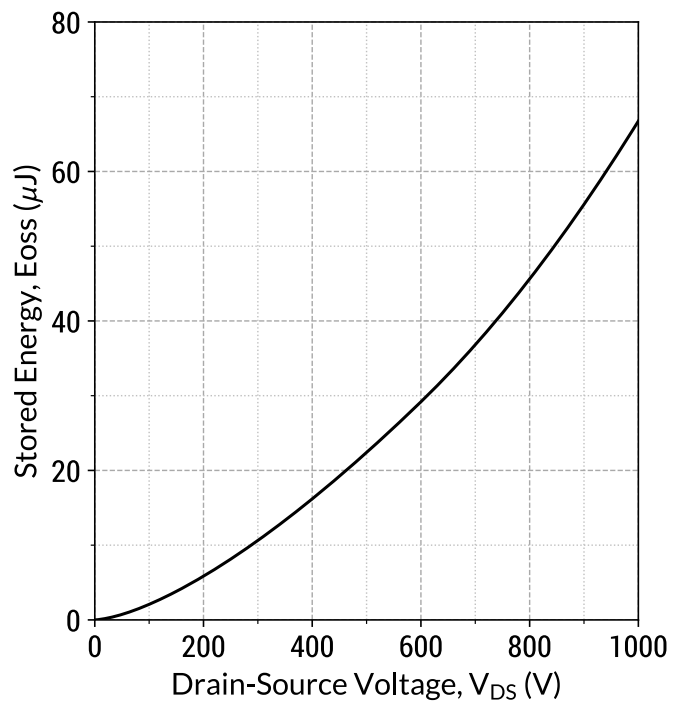
$I_D = 45 \text{ A}; V_{DS} = 800 \text{ V}; T_c = 25^\circ\text{C}$

Figure 11: Capacitance v/s Drain-Source Voltage



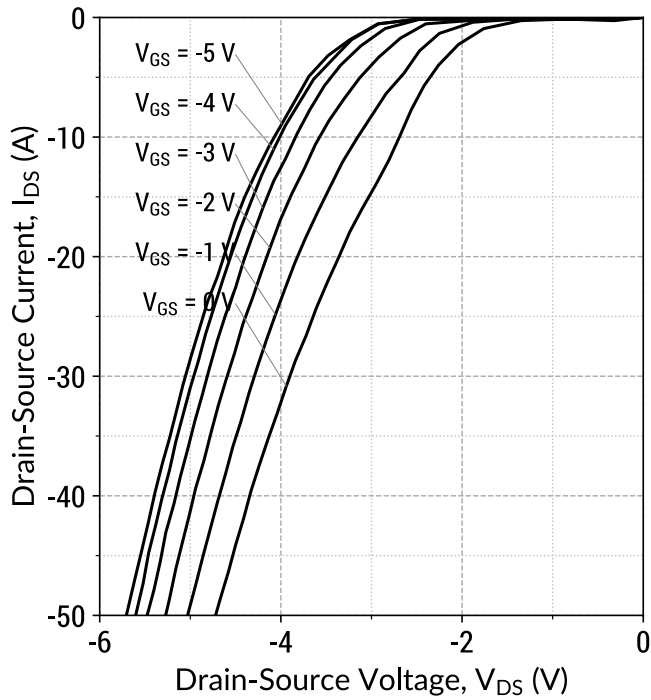
$f = 1 \text{ MHz}; V_{AC} = 25 \text{ mV}$

Figure 12: Output Capacitor Stored Energy



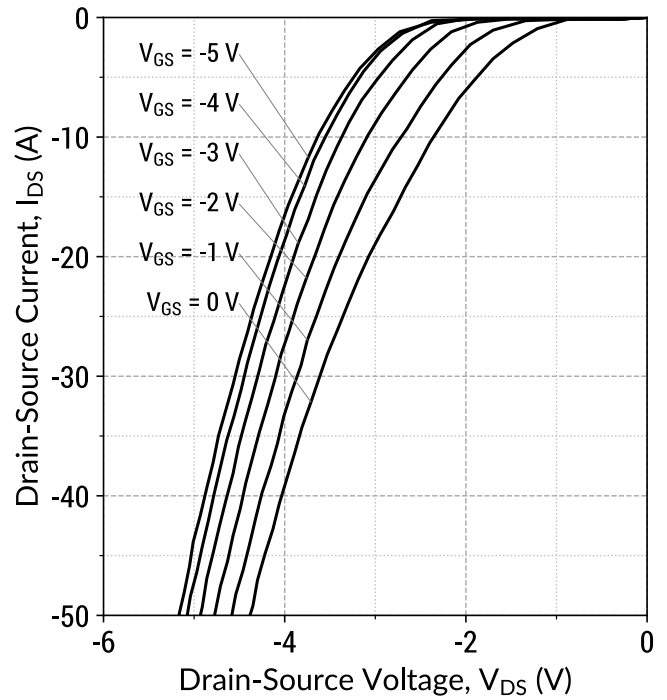
$E_{oss} = f(V_{DS})$

Figure 13: Body Diode Characteristics ( $T_j = 25^\circ\text{C}$ )



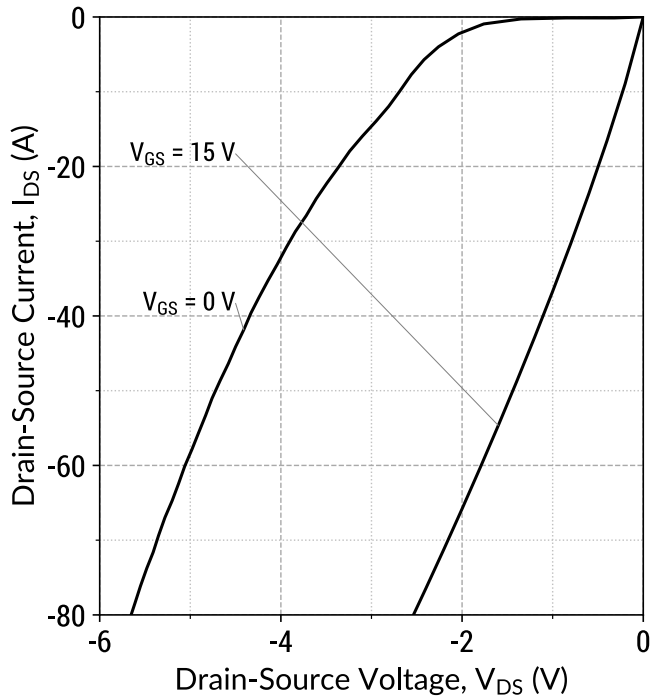
$$I_D = f(V_{DS}, V_{GS}); t_P = 250 \mu\text{s}$$

Figure 14: Body Diode Characteristics ( $T_j = 200^\circ\text{C}$ )



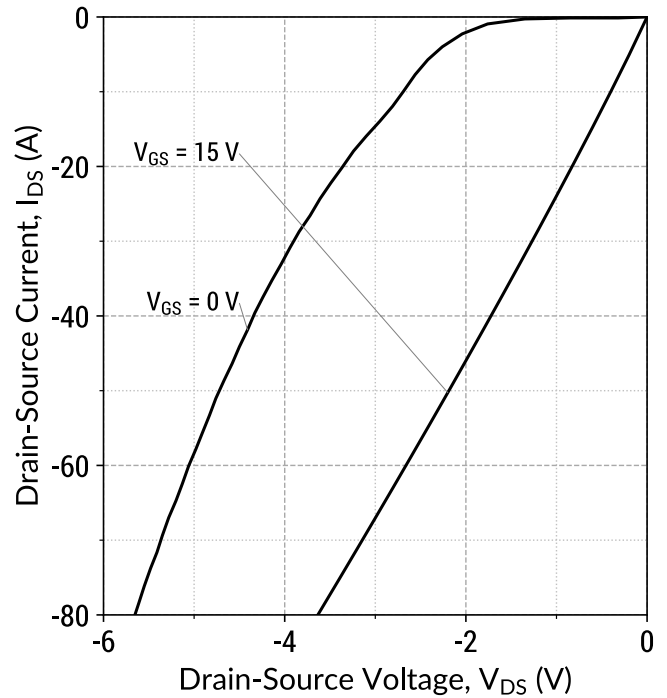
$$I_D = f(V_{DS}, V_{GS}); t_P = 250 \mu\text{s}$$

Figure 15: Third Quadrant Characteristics ( $T_j = 25^\circ\text{C}$ )



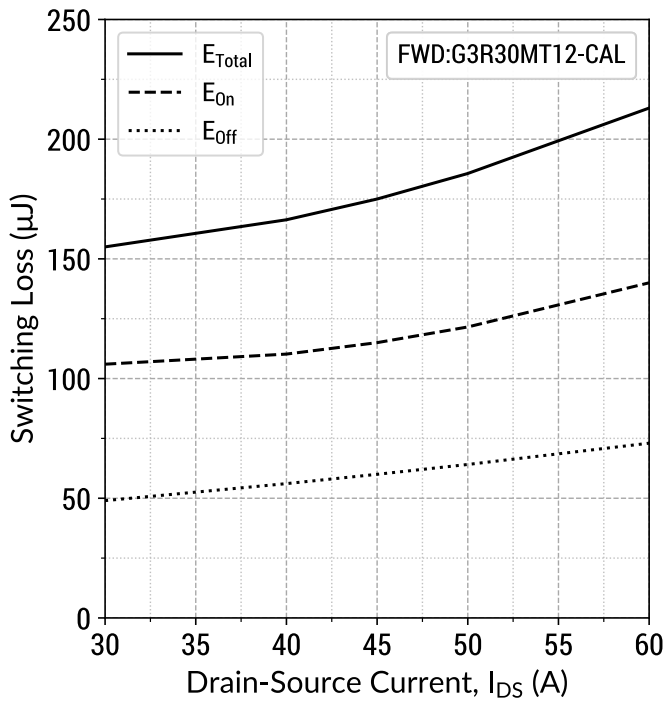
$$I_D = f(V_{DS}, V_{GS}); t_P = 250 \mu\text{s}$$

Figure 16: Third Quadrant Characteristics ( $T_j = 200^\circ\text{C}$ )



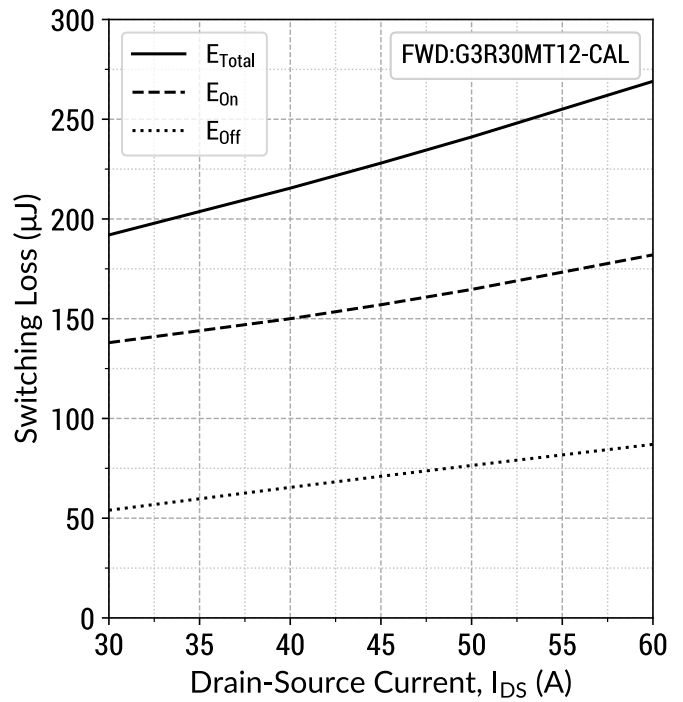
$$I_D = f(V_{DS}, V_{GS}); t_P = 250 \mu\text{s}$$

Figure 17: Resistive Switching Energy v/s Drain Current  
( $V_{DD} = 600V$ )



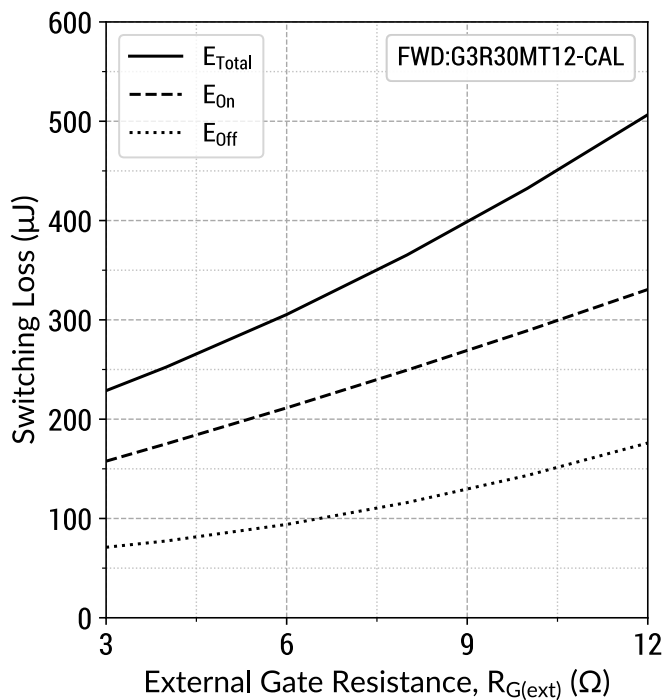
$T_j = 25^\circ C$ ;  $V_{GS} = -5/+15V$ ;  $R_{G(ext)} = 3 \Omega$

Figure 18: Resistive Switching Energy v/s Drain Current  
( $V_{DD} = 800V$ )



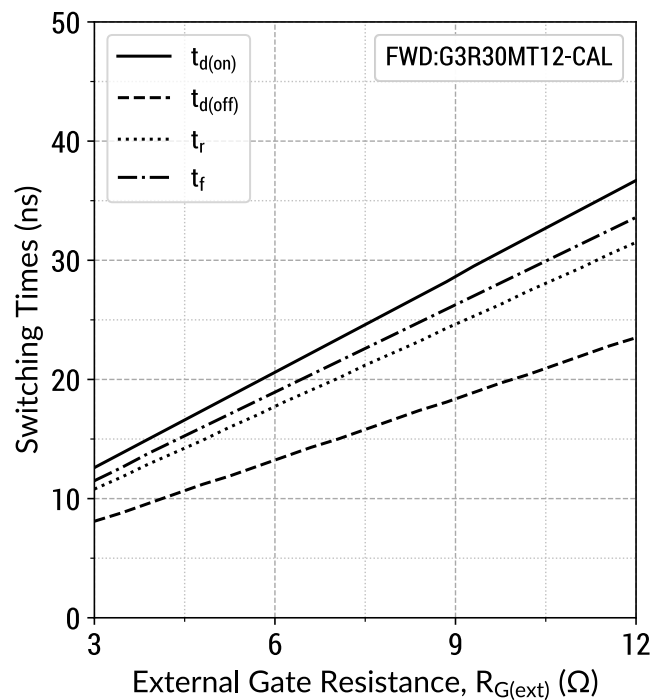
$T_j = 25^\circ C$ ;  $V_{GS} = -5/+15V$ ;  $R_{G(ext)} = 3 \Omega$

Figure 19: Resistive Switching Energy v/s  $R_{G(ext)}$   
( $V_{DD} = 800V$ )



$T_j = 25^\circ C$ ;  $V_{GS} = -5/+15V$ ;  $I_{DS} = 45 A$

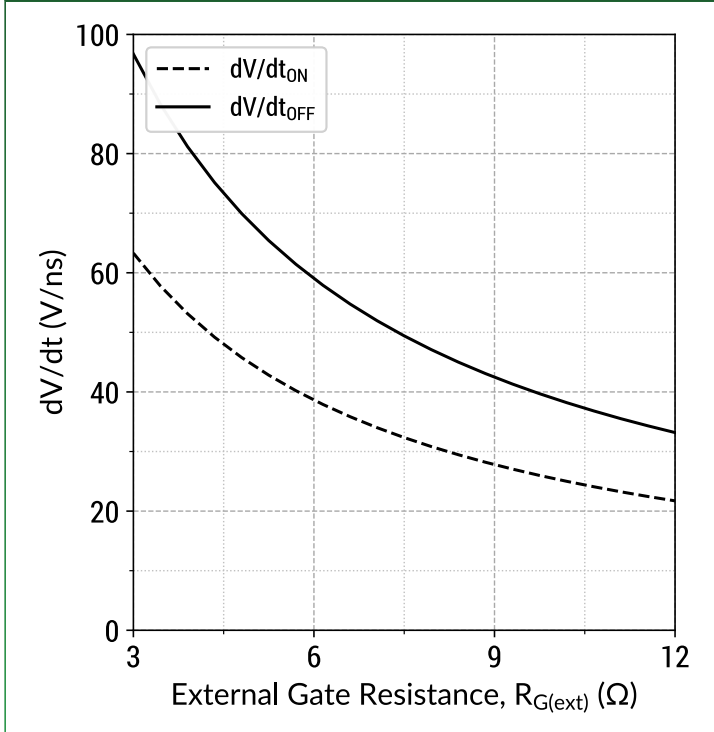
Figure 20: Switching Time v/s  $R_{G(ext)}$   
( $V_{DD} = 800V$ )



$T_j = 25^\circ C$ ;  $V_{GS} = -5/+15V$ ;  $I_{DS} = 45 A$

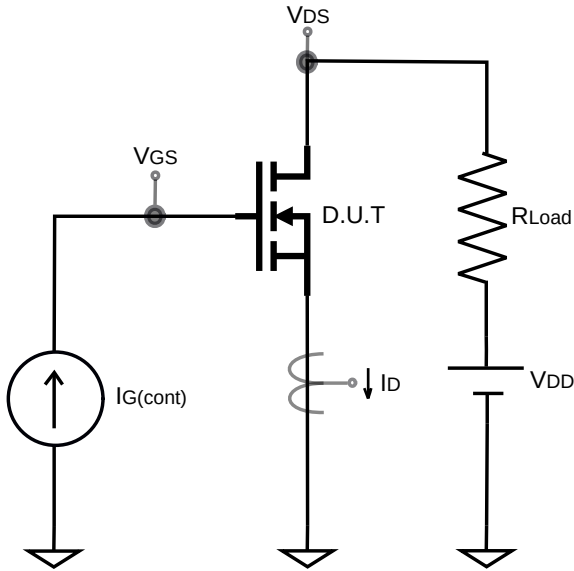


Figure 21:  $dV/dt$  v/s  $R_{G(ext)}$   
( $V_{DD} = 800V$ )

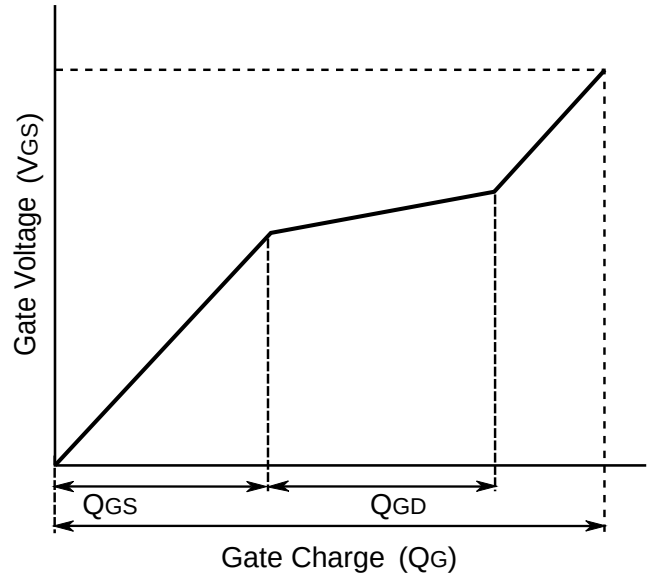


$T_j = 25^\circ C$ ;  $V_{GS} = -5/+15V$ ;  $I_{BS} = 45 A$

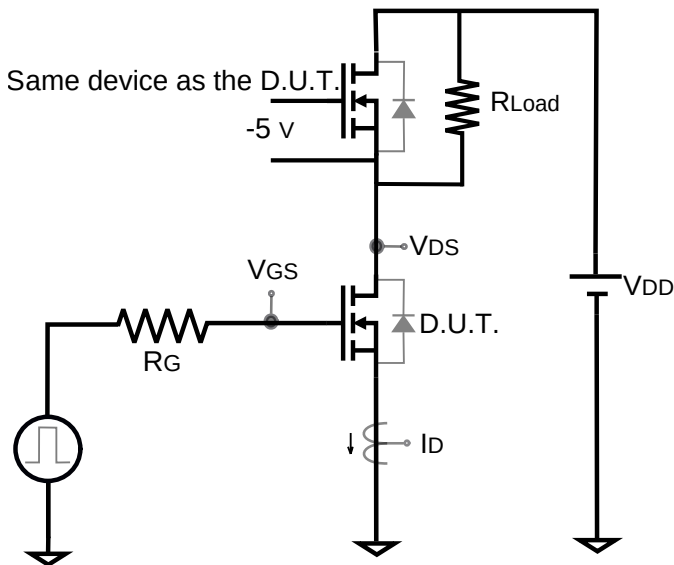
Gate Charge Circuit



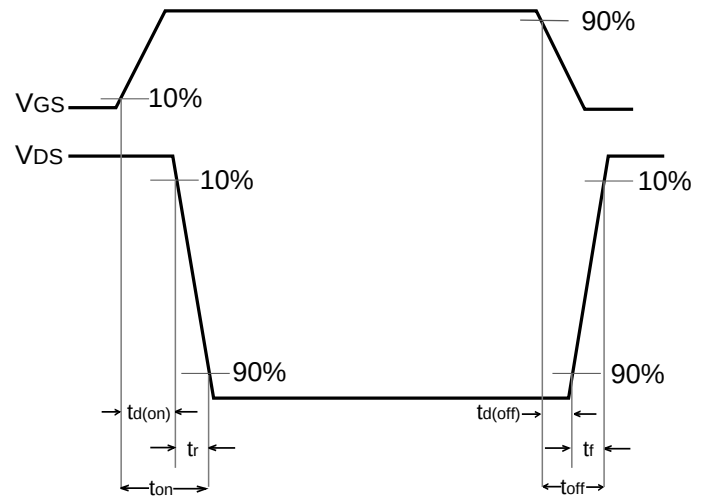
Gate Charge Waveform



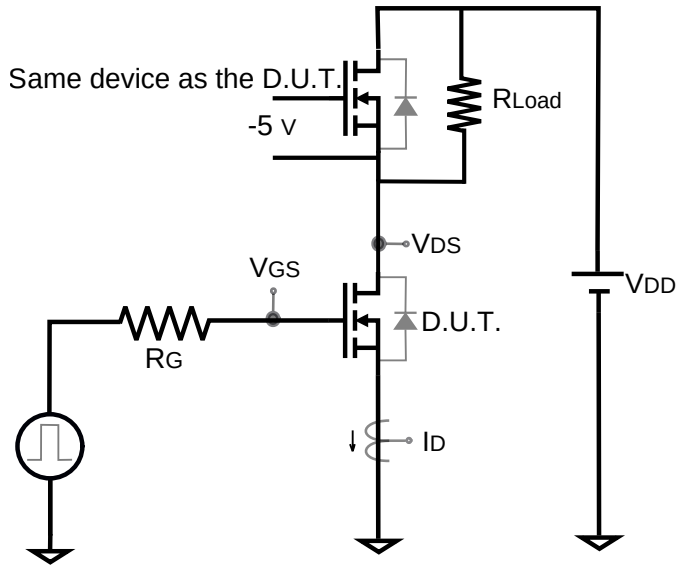
Switching Time Circuit



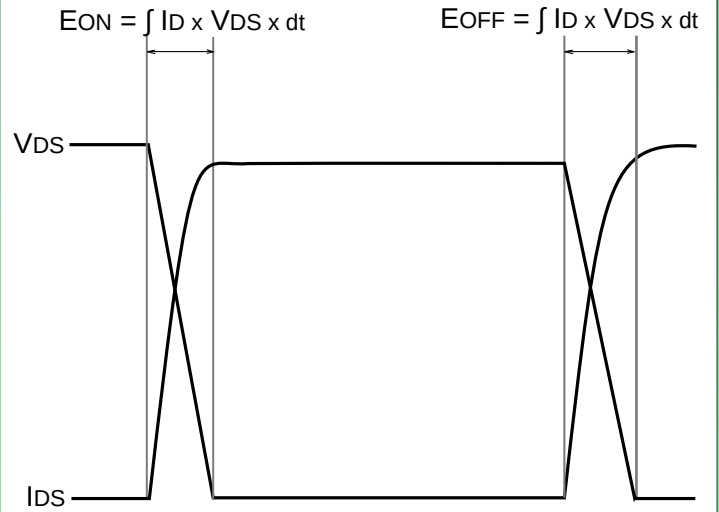
Switching Time Waveform



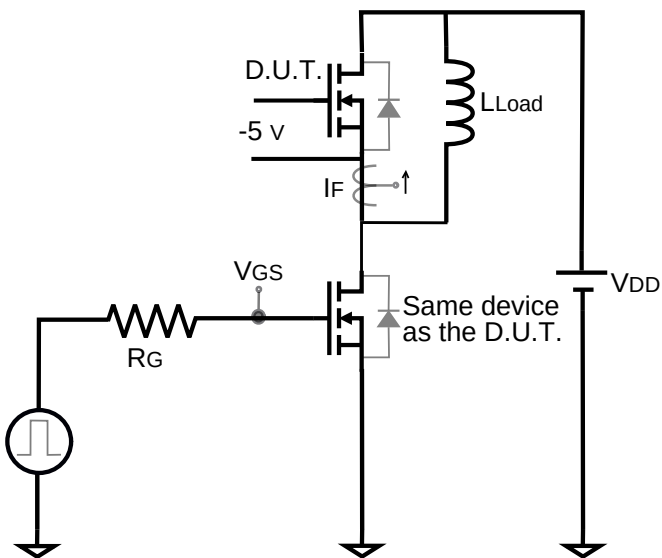
Switching Energy Circuit



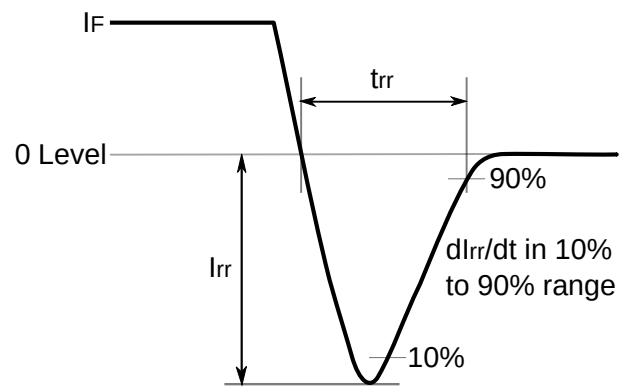
Switching Energy Waveform



Reverse Recovery Circuit



Reverse Recovery Waveform



### Mechanical Parameters

This information is **confidential**, please contact [sales@genesicsemi.com](mailto:sales@genesicsemi.com) to learn more.

### Chip Dimensions

This information is **confidential**, please contact [sales@genesicsemi.com](mailto:sales@genesicsemi.com) to learn more.

#### NOTE

1. CONTROLLED DIMENSION IS MILLIMETER.
2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.

## Compliance

### RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

### REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

## Disclaimer

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Unless otherwise expressly indicated, GeneSiC products are not designed, tested or authorized for use in life-saving, medical, aircraft navigation, communication, air traffic control and weapons systems, nor in applications where their failure may result in death, personal injury and/or property damage.

## Related Links

- SPICE Models: [https://www.genesicsemi.com/sic-mosfet/G3R30MT12-CAL/G3R30MT12-CAL\\_SPICE.zip](https://www.genesicsemi.com/sic-mosfet/G3R30MT12-CAL/G3R30MT12-CAL_SPICE.zip)
- PLECS Models: [https://www.genesicsemi.com/sic-mosfet/G3R30MT12-CAL/G3R30MT12-CAL\\_PLECS.zip](https://www.genesicsemi.com/sic-mosfet/G3R30MT12-CAL/G3R30MT12-CAL_PLECS.zip)
- CAD Models: [https://www.genesicsemi.com/sic-mosfet/G3R30MT12-CAL/G3R30MT12-CAL\\_3D.zip](https://www.genesicsemi.com/sic-mosfet/G3R30MT12-CAL/G3R30MT12-CAL_3D.zip)
- Gate Driver Reference: <https://www.genesicsemi.com/technical-support>
- Evaluation Boards: <https://www.genesicsemi.com/technical-support>
- Reliability: <https://www.genesicsemi.com/reliability>
- Compliance: <https://www.genesicsemi.com/compliance>
- Quality Manual: <https://www.genesicsemi.com/quality>

## Revision History

- Rev 21/Jun: Updated switching time and switching energy data
- Supersedes: Rev 20/Jun, Rev 20/Aug, Rev 21/Jan



[www.genesicsemi.com/sic-mosfet/](https://www.genesicsemi.com/sic-mosfet/)